



At Riester, we understand the needs and challenges of our customers in their daily medical routines. Our goal worldwide is to help healthcare providers improve the lives of patients. Through innovation, cutting-edge technology, and our commitment to excellence, our portfolio of diagnostic devices is setting new standards.

Based at our Head Office in **Jungingen**, we are looking for **several interns...**

Intern Electronic-Development

Tasks to include:

- Design of analogue and digital circuits
- Design of Schematics and PCB layouts
- Assembly and testing of prototypes and their functionality

Your Profile:

- Studies in Electrical Engineering - Electronics - Information Technology or similar technical disciplines
- Knowledge of analogue and digital circuit technology and electronics
- Experience in dealing with modern measurement equipment
- Layout experience with Eagle, Mentor Graphics, Altium Designer or similar PCB layout tools
- Basic knowledge of the programming language C / C ++
- An independent, committed, goal-oriented and well-structured way of working
- A cross divisional and quality conscious way of thinking, along with the ability to work as part of a team
- Excellent command of German and English (spoken and written)

We offer:

- The opportunity to work in an innovative, friendly team in a very pleasant working atmosphere, with flat hierarchies and efficient decision-making processes
- Work in a versatile field with challenging tasks, for a future market

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SWIFT/BIC: DEUTDESS653
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Erfüllungsort für Lieferung und
Zahlung: Jungingen
Gerichtsstand: Hechingen.
Die Ware bleibt bis zur vollständigen
Bezahlung unser Eigentum.